

PATENT
2834-119P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: GUDESEN, Hans Gude et al
Appl. No. 09/463,900 Group:
Filed: February 2, 2000 Examiner:
For: SCALABLE INTEGRATED DATA PROCESSING
DEVICE



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Am stt
B
G. Stanley
3-13-03

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, DC 20231

June 22, 2000

Sir:

The following Preliminary Amendments and Remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS

Please cancel claims 1-16.

Please add the following new claims 17-32.

B1
17. A scaleable integrated data processing device, provided on a carrier substrate (S), comprising a processing unit having one or more processors, and a storage unit having one or more memories,

wherein the data processing device comprises mutually adjacent, substantially parallel stacked layers (P, M, MP) and the processing unit and the storage unit are provided in one or more of the substantially parallel stacked layers,

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